

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))					Docket No. 18497 #4	
In Re Application Of: Takashi Nakano, et al.						
Application No. 10/518,157	Filing Date December 10, 2004	Examiner unassigned	Customer No. 23389	Group Art Unit 2812	Confirmation No. 3521	
Title: SEMICONDUCTOR DEVICE, SEMICONDUCTOR CIRCUIT AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE						
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450						
37 CFR 1.97(b)						
1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.						
37 CFR 1.97(c)						
2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:						
<input type="checkbox"/> the statement specified in 37 CFR 1.97(e);						
OR						
<input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).						

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(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
18497

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10/518,157	December 10, 2004	unassigned	23389	2812	3521

Title: **SEMICONDUCTOR DEVICE, SEMICONDUCTOR CIRCUIT AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE**

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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